

Descriptions

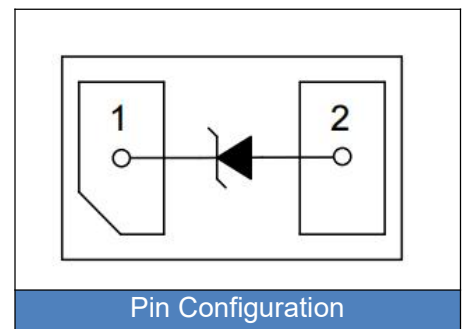
The SP1P882A07P01 is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computer, and PDAs. It offers superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. It is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).



Features

- ◆ IEC61000-4-2 ESD 30KV Air, 30KV contact compliance
- ◆ SOD882 surface mount package
- ◆ Working voltage: 7.0V
- ◆ Low leakage current
- ◆ Low operating and clamping voltages
- ◆ Solid-state silicon avalanche technology
- ◆ RoHS compliant
- ◆ Solder reflow temperature: Pure Tin-Sn, 260~270°C
- ◆ Flammability rating UL 94V-0
- ◆ Meets MSL level 1, per J-STD-020

Schematic



Applications

- ◆ Cellular handsets & Accessories
- ◆ Notebooks & Handhelds
- ◆ Digital Cameras
- ◆ Personal Digital Assistants (PDAs)
- ◆ Portable Instrumentation

Dimensions (SOD-882)

Symbol	Dimension			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.95	1.05	0.037	0.041
B	0.55	0.65	0.022	0.026
C	0.32	0.55	0.013	0.022
D	0.45		0.018	
E	0.20	0.30	0.008	0.012
F	0.45	0.55	0.018	0.022

Maximum Ratings($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	V_{ESD}	± 30	kV
ESD voltage (Air discharge)		± 30	
Storage & operating temperature range	T_{STG}, T_J	$-55 \sim +150$	$^{\circ}\text{C}$

Electrical Characteristics($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Reverse stand-off voltage	V_{RWM}				7.0	V
Reverse breakdown voltage	V_{BR}	$I_{\text{BR}}=1.0\text{mA}$	8.0			V
Reverse leakage current	I_{R}	$V_{\text{R}}=7.0\text{V}$			0.1	μA
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_{C}	$I_{\text{PP}}=1\text{A}$			11	V
Clamping voltage ($t_p=8/20\mu\text{s}$)	V_{C}	$I_{\text{PP}}=9.5\text{A}$			15	V
Peak Pulse Current($t_p=8/20\mu\text{s}$)	I_{PP}				9.5	A
Off state junction capacitance	C_J	$0\text{Vdc}, f=1\text{MHz}$		49	65	pF

Ratings and Characteristic Curves (T_A=25°C unless otherwise noted)

Figure 1. Pulse Waveforms

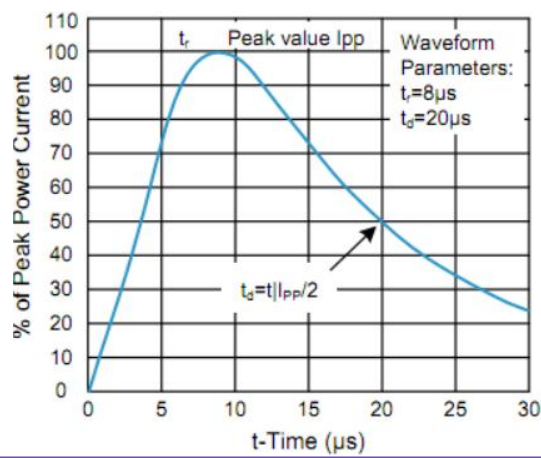


Figure 2. Clamping Voltage vs. Peak Pulse Current

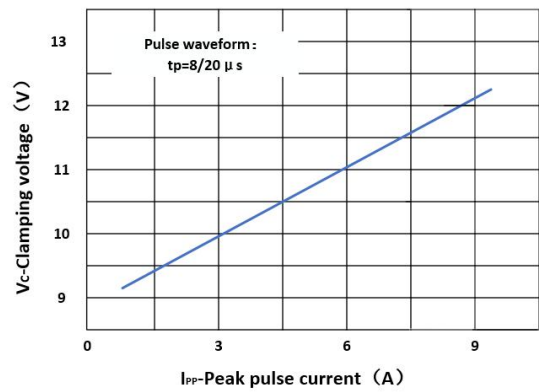


Figure 3. Capacitance vs. Reverse Voltage

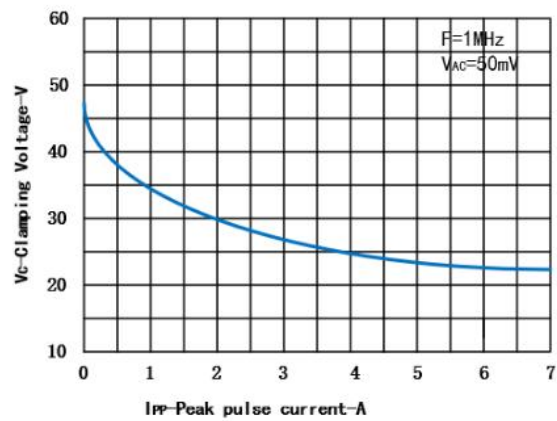
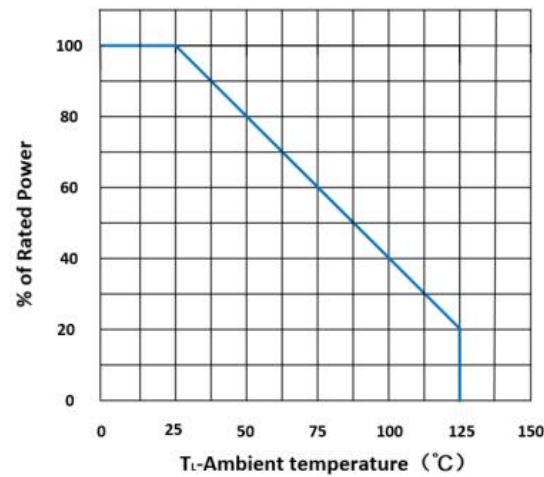
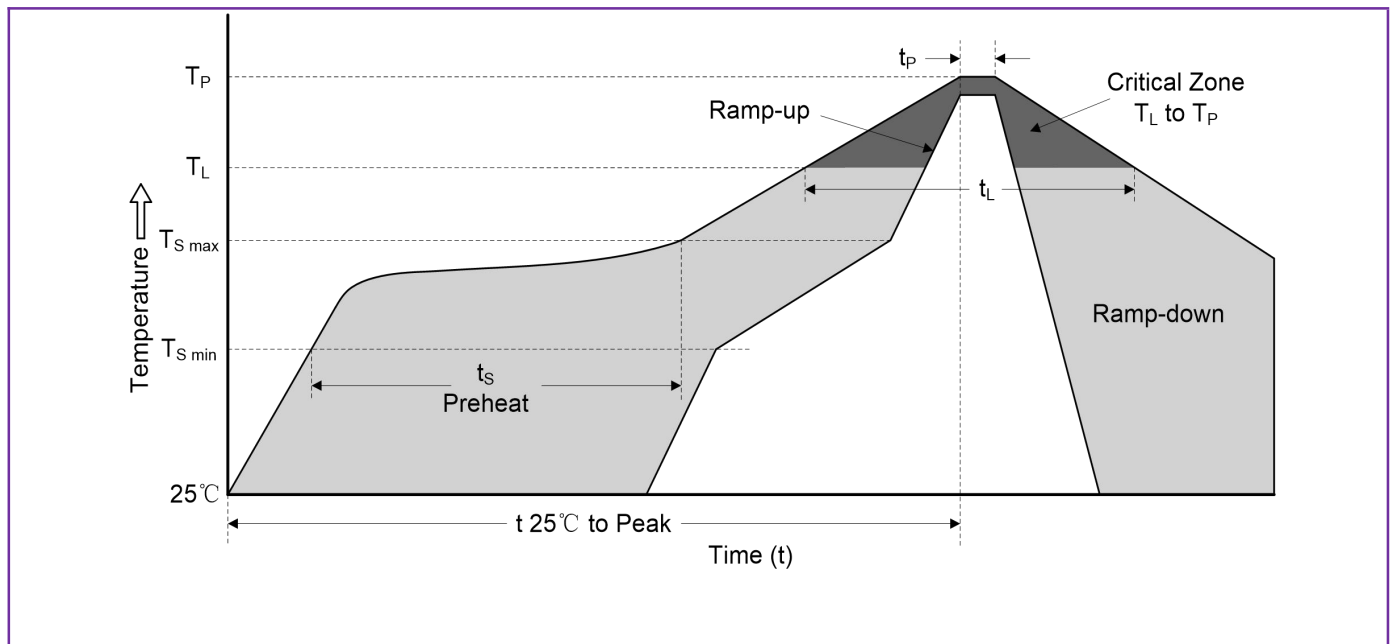


Figure 4. Power Derating Curve



Reflow Soldering Parameters



Reflow Condition		Lead-free Assembly
Pre heat	-Temperature Min ($T_{S\ min}$)	150°C
	-Temperature Max ($T_{S\ max}$)	200°C
	-Time (min to max) (t_s)	60-180 seconds
Average ramp-up rate (T_L to T_P)		3°C/second max.
$T_{S\ max}$ to T_L -Ramp-up Rate		3°C/second max.
Reflow	-Temperature (T_L) (Liquidus)	217°C
	-Time (min to max) (t_s)	60-150 seconds
Peak Temperature (T_P)		260(+0/-5)°C
Time within 5°C of actual Peak Temperature (t_P)		20-40 seconds
Ramp-down Rate		6°C/second max.
Time 25°C to Peak Temperature(T_P)		8 minutes max.
Do not exceed		260°C

Packaging

Tape		Symbol	Dimension (mm)
		W	8.00±0.30
		P0	4.00±0.10
		P1	2.00±0.10
		P2	2.00±0.10
		D0	Φ1.55±0.10
		D1	Φ0.40±0.05
		E	1.75±0.10
		F	3.50±0.10
		A	0.75±0.10
		B	1.15±0.10
		K	0.60±0.05
		t	0.20±0.05
Reel		D	Φ178.0±2.0
		D2	Φ13.00
		W1	9.50
		Quantity: 10000PCS	